Brief History of the EMU Trigger Motherboard (TMB)

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1998: TMB98

Altera EPF10K50 PLD

- First TMB built at Rice
- CAMAC readout
- 3 Altera EPF10K50 PLDs
- For ’98 beam test
1999: TMB99

- Second TMB built at Rice
- First 9U VME Prototype
- 3 Altera EPF10K50/30 PLDs
- For ’99 beam test
2001: TMB2001

- First TMB built at UCLA
- Initial XCV1000E-based mezzanine board
- PHOS4-based delays
- Initial version of the ALCT and RAT connection
2004: TMB2004

- PHOS4 delay chips (TMB2001) were replaced with 3D3444
- New XC2V4000-based mezzanine board
- New radiation tolerant voltage regulator
2005: TMB2005

- Production Version
- Same XC2V4000-based mezzanine as TMB2004
- Modified PCB layout
- Modified connection to ALCT and RAT boards (through the RAT board)