Pin assignment for the boards in the peripheral crate

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- Zpack125 on schematics is AMP part number 100145-1 (female 25 rows by 5 pins).
- Zpack55 on schematics is AMP part number 100161-1 (female 11 rows by 5 pins).
- The guiding pins will be implemented on the backplane-strengthening bars. The corresponding guiding modules are shown on the mechanical drawings (AMP part number 223957-1). These modules are used also for keying the boards.
- All signal names match those in Jonathan’s document (EMU Peripheral Crate Specification, version 3.2).
- All bussed signals must NOT be terminated on boards, the tracks from the connector to GTLP transceivers should be done as short as possible.
- All 40 MHz point-to-point signals must be terminated on receiving boards: 100 OHM to 1.5 V for GTLP, 100 OHM between the complementary signals for LVDS (clock), as close to the receiver as possible.
- All 80 MHz signals must be terminated on receiving boards: 50 OHM to 1.5 V for GTLP, as close to the receiver as possible.
- 1.5 V power from the backplane is provided for GTLP termination. The board designer should provide bypassing for it. MPC is powered from two regulators, so there are two 1.5V inputs. The load must be distributed so that each of the regulators provides exactly one half of the total power necessary to terminate all GTLP signals.
- TMB RPC feed-thru connector (X18) pin assignment is not defined, all pins are left unconnected on the backplane.
- Mechanical drawings for connector placement are available here:
  
  http://www.phys.ufl.edu/~madorsky/backplane/ccb.dxf
  http://www.phys.ufl.edu/~madorsky/backplane/dmb.dxf
  http://www.phys.ufl.edu/~madorsky/backplane/mpc.dxf
  http://www.phys.ufl.edu/~madorsky/backplane/tmb.dxf

  Download these files to your local disk and then open by the application supporting DXF – OrCAD (use VisualCAD tool), AutoCAD, Volo View (available for free from http://www.download.com/), and by many other applications.
CCB
Revision history:

- 02/05/01 – Creation
- 02/06/01 – Title changed to reflect better the purpose of this document
- 02/12/01 – Clock_Enable+ renamed to Clock_Enable, Clock_Enable- renamed to ccb_reserved4
- 02/22/01 – Universal Power Modules added to TMB, DMB, CCB. MPC 1.5 V power is split in two halves.
- 02/26/01 – Mechanical drawings links added.
- 03/02/01 – Universal Power Modules excluded, all board’s connectors are changed to female type, guiding hardware added. Mechanical drawings updated.