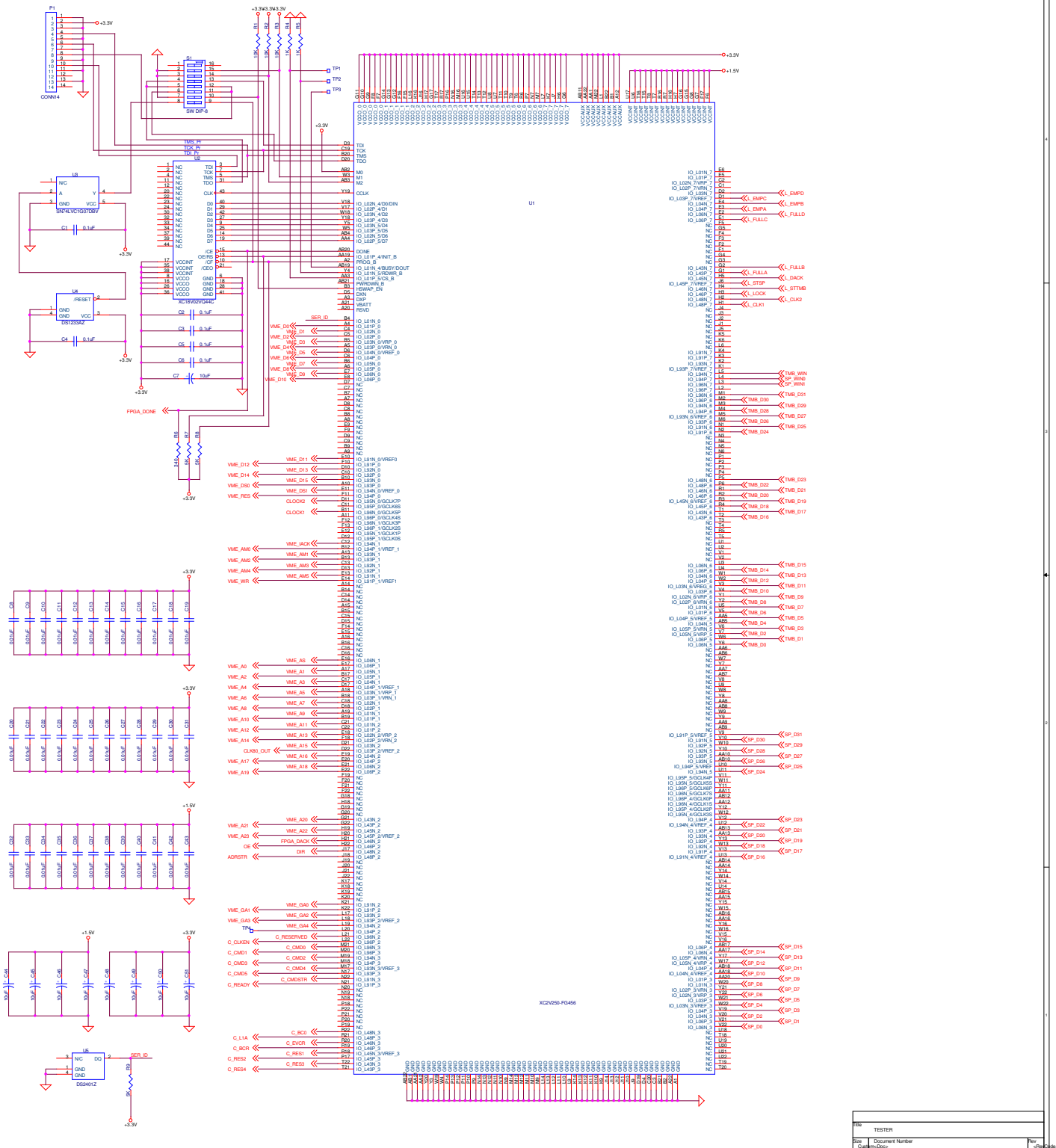
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CONN14
S1
SW DIP-8
U1
XC2V250-FG456
U2
XC18V02VQ44C
U3
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U4
DS1233AZ
U5
DS2401Z
TP1
TP2
TP3
TP4
+3.3V
+1.5V
FPGA_DONE
SER_ID
VME_D0
VME_D1
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VME_DS0
VME_DS1
VME_RES
CLOCK2
CLOCK1
VME_IACK
VME_AM0
VME_AM1
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VME_AM5
VME_WR
VME_AS
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VME_A22
VME_A23
FPGA_DACK
OE
DIR
ADRSTR
VME_GA0
VME_GA1
VME_GA2
VME_GA3
VME_GA4
C_RESERVED
C_CLKEN
C_CMD0
C_CMD1
C_CMD2
C_CMD3
C_CMD4
C_CMD5
C_CMDSTR
C_READY
C_L1A
C_BC0
C_BCR
C_EVCR
C_RES1
C_RES2
C_RES3
C_RES4
L_EMPD
L_EMPC
L_EMPB
L_EMPA
L_FULLD
L_FULLC
L_FULLB
L_FULLA
L_DACK
L_STSP
L_STTMB
L_LOCK
L_CLK2
L_CLK1
TMB_WIN
SP_WIN2
SP_WIN3
TMB_D31
TMB_D30
TMB_D29
TMB_D28
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Date: Wednesday, February 04, 2004
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